

### ENA EREC G98/NI:2019

Type Approval and Manufacturer declaration of compliance with the requirements of G98/NI.

This form should be used when making a Type Test submission to the Energy Networks

Association (ENA).

If the Micro-generator is Fully Type Tested and already registered with the ENA Type Test Verification Report Register, the Installation Document should include the Manufacturer's Reference Number (the Product ID), and this form does not need to be submitted.

Where the Micro-generator is not registered with the ENA Type Test Verification Report

Register this form needs to be completed and provided to the DNO, to confirm that the Microgenerator has been tested to satisfy the requirements of this EREC G98/NI.

| SSEG Type reference number |                       | HM-1500,HM-1200                                      |   |                  |  |  |  |
|----------------------------|-----------------------|--|---|------------------|--|--|--|
| SSEG Type                  |                       | Photovoltaic Microinverter                           |   |                  |  |  |  |
| System Supplier name       |                       | Hoymiles Po  | Hoymiles Power Electronics Inc.                                 |                  |  |  |  |
| Address                    |                       | No.18 Kangj<br>P.R. China                            | No.18 Kangjing road, HangZhou, Zhejiang Province,<br>P.R. China |                  |  |  |  |
| Tel                        | +86 571 28056101      |  | Fax   | +86 571 28056137 |  |  |  |
| E:mail                     | zhangxingyao@hzo<br>m | converter.co   | Web site  | www.hoymiles.com |  |  |  |
| Maximum<br>rated           |                       | Connection Option                                    |   |                  |  |  |  |
| capacity,<br>use           | 1.5/1.2               | kW single phase, single, split or three phase system |   |                  |  |  |  |
| separate<br>sheet if       | NA                    | kW three pha   | ase   |                  |  |  |  |
| more than<br>one           | NA                    | kW two phases in three phase system                  |   |                  |  |  |  |
| connection option.         | NA                    | kW two phas  | kW two phases split phase system                                |                  |  |  |  |

SSEG manufacturer/supplier declaration

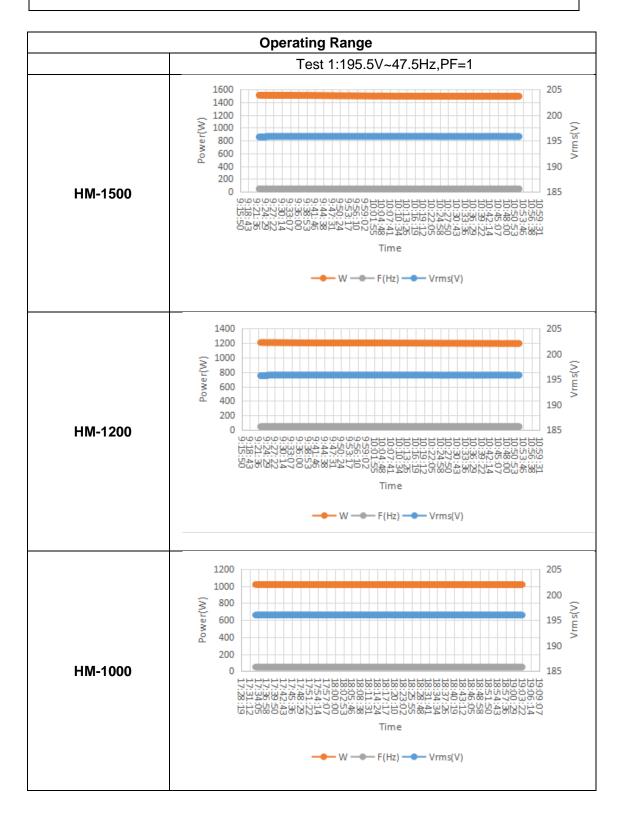
Manufacturer Type Test declaration. - I certify that all products supplied by the company with the above Type Tested reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site modifications are required to ensure that the product meets all the requirements of EREC G98/NI.

| Signed 建发湖 | On behalf of | Hoymiles Power Electronics Inc. |
|------------|--------------|---------------------------------|
|------------|--------------|---------------------------------|

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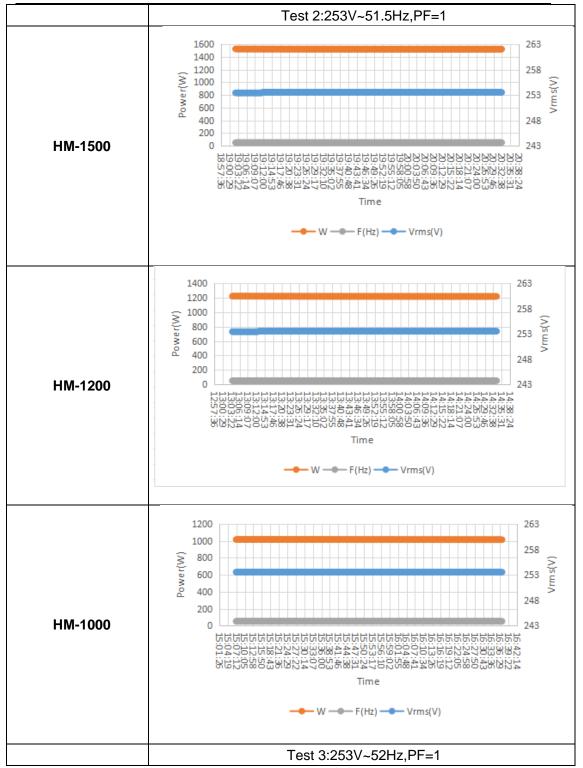
Note that testing can be done by the Manufacturer of an individual component or by an external test house.

Where parts of the testing are carried out by persons or organisations other than the Manufacturer then that person or organisation shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.

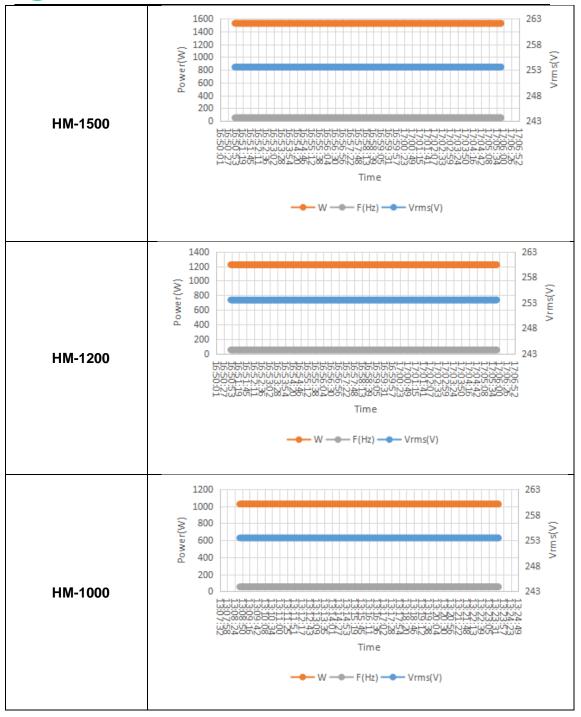


TRP-21060903









| <b>Power Quality</b> – <b>Harmonics:</b> These tests should be carried out as specified in BS EN 61000-3-2. |                                    |            |             |                |  |  |  |  |
|---|------------------------------------|------------|-------------|----------------|--|--|--|--|
| SSE   | SSEG rating per phase (rpp) 1.5 kW |            |             |                |  |  |  |  |
| Harmo<br>nic  | At 45-55% of rated output          | 100% of ra | ated output | NV=MV*3.68/rpp |  |  |  |  |



|    |           |            |           |            | Limit in     | Higher           |
|----|-----------|------------|-----------|------------|--------------|------------------|
|    | Measured  | Normalised | Measured  | Normalised | BS EN        | limit for<br>odd |
|    | Value(MV) | Value (NV) | Value(MV) | Value (NV) | 61000-3-     | harmonic         |
|    | in Amps   | In Amps    | In Amps   | In Amps    | 2<br>in Amna | s 21 and         |
|    |           |            |           |            | in Amps      | above            |
| 2  | 0.0189    | 0.0464     | 0.039     | 0.0957     | 1.080        |                  |
| 3  | 0.0112    | 0.0275     | 0.0344    | 0.0844     | 2.300        |                  |
| 4  | 0.0082    | 0.0201     | 0.0126    | 0.0309     | 0.430        |                  |
| 5  | 0.0141    | 0.0346     | 0.0453    | 0.1111     | 1.140        |                  |
| 6  | 0.0053    | 0.013      | 0.0095    | 0.0233     | 0.300        |                  |
| 7  | 0.0201    | 0.0493     | 0.0386    | 0.0947     | 0.770        |                  |
| 8  | 0.0048    | 0.0118     | 0.0064    | 0.0157     | 0.230        |                  |
| 9  | 0.0148    | 0.0363     | 0.0388    | 0.0952     | 0.400        |                  |
| 10 | 0.0034    | 0.0083     | 0.0053    | 0.013      | 0.184        |                  |
| 11 | 0.0077    | 0.0189     | 0.0424    | 0.104      | 0.450        |                  |
| 12 | 0.0024    | 0.0059     | 0.0044    | 0.0108     | 0.153        |                  |
| 13 | 0.0074    | 0.0182     | 0.0333    | 0.0817     | 0.210        |                  |
| 14 | 0.0021    | 0.0052     | 0.0093    | 0.0228     | 0.131        |                  |
| 15 | 0.0056    | 0.0137     | 0.0251    | 0.0616     | 0.150        |                  |
| 16 | 0.0018    | 0.0044     | 0.0076    | 0.0186     | 0.115        |                  |
| 17 | 0.0084    | 0.0206     | 0.0182    | 0.0447     | 0.132        |                  |
| 18 | 0.0024    | 0.0059     | 0.0093    | 0.0228     | 0.102        |                  |
| 19 | 0.0052    | 0.0128     | 0.0141    | 0.0346     | 0.118        |                  |
| 20 | 0.0024    | 0.0059     | 0.0046    | 0.0113     | 0.092        |                  |
| 21 | 0.0064    | 0.0157     | 0.0127    | 0.0312     | 0.107        |                  |
| 22 | 0.0015    | 0.0037     | 0.0074    | 0.0182     | 0.084        |                  |
| 23 | 0.0085    | 0.0209     | 0.0148    | 0.0363     | 0.098        | 0.147            |
| 24 | 0.0023    | 0.0056     | 0.0075    | 0.0184     | 0.077        |                  |

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| 25 | 0.0064 | 0.0157 | 0.0181 | 0.0444 | 0.090 | 0.135 |
|----|--------|--------|--------|--------|-------|-------|
| 26 | 0.0027 | 0.0066 | 0.0088 | 0.0216 | 0.071 |       |
| 27 | 0.0043 | 0.0105 | 0.0175 | 0.0429 | 0.083 | 0.124 |
| 28 | 0.0004 | 0.001  | 0.007  | 0.0172 | 0.066 |       |
| 29 | 0.004  | 0.0098 | 0.0138 | 0.0339 | 0.078 | 0.117 |
| 30 | 0.0022 | 0.0054 | 0.0105 | 0.0258 | 0.061 |       |
| 31 | 0.0028 | 0.0069 | 0.0123 | 0.0302 | 0.073 | 0.109 |
| 32 | 0.0021 | 0.0052 | 0.0108 | 0.0265 | 0.058 |       |
| 33 | 0.0069 | 0.0169 | 0.0132 | 0.0324 | 0.068 | 0.102 |
| 34 | 0.0006 | 0.0015 | 0.0069 | 0.0169 | 0.054 |       |
| 35 | 0.008  | 0.0196 | 0.0117 | 0.0287 | 0.064 | 0.096 |
| 36 | 0.001  | 0.0025 | 0.0074 | 0.0182 | 0.051 |       |
| 37 | 0.005  | 0.0123 | 0.0081 | 0.0199 | 0.061 | 0.091 |
| 38 | 0.0018 | 0.0044 | 0.0096 | 0.0236 | 0.048 |       |
| 39 | 0.0067 | 0.0164 | 0.0083 | 0.0204 | 0.058 | 0.087 |
| 40 | 0.0005 | 0.0012 | 0.0095 | 0.0233 | 0.046 |       |
|    |        |        |        |        |       |       |

Note the higher limits for odd harmonics 21 and above are only allowable under certain conditions, if these higher limits are utilised please state the exemption used as detailed in part 6.2.3.4 of BS EN 61000-3-2 in the box below.

| Power Quality. Voltage fluctuations and Flicker.                           |             |          |             |             |          |             |       |                |  |
|--|-------------|----------|-------------|-------------|----------|-------------|-------|----------------|--|
|  |             | Starting |             |             | Stopping |             |       | Running        |  |
|  | dmax<br>[%] | dc [%]   | d(t)<br>[%] | dmax<br>[%] | dc [%]   | d(t)<br>[%] | Pst   | Plt 2<br>hours |  |
| Measured Values  | 0.1         | 0        | 0           | 0.1         | 0        | 0           | 0.064 | 0.064          |  |
| Normalised to<br>standard<br>impedance<br>and 3.68kW for<br>multiple units | 0.3         | 0        | 0           | 0.3         | 0        | 0           | 0.2   | 0.2            |  |

### Power Quality. Voltage fluctuations and Flicker.



| Limits set under<br>BS EN 61000-3-2 | 4%                                  | 3.30%   | 3.3%<br>500ms | 4% | 3.30% | 3.3%<br>500ms | 1 | 0.65 |
|-------------------------------------|-------------------------------------|---|---------------|----|-------|---------------|---|------|
| Test start date                     | 2020-07-31 Test end date 2020-07-31 |   |               |    |       |               |   |      |
| Test location                       |                                     | SHANGHAI TESTING & INSPECTION INSTITUTE<br>FOR ELECTRICAL EQUIPMENT CO., LTD. |               |    |       |               |   |      |

| <b>Power quality</b> – <b>DC injection:</b> This test should be carried out in accordance with EN 50438 Annex D.3.10 |        |        |        |        |  |  |  |  |  |
|--|--------|--------|--------|--------|--|--|--|--|--|
| Test power level   | 20%    | 50%    | 75%    | 100%   |  |  |  |  |  |
| Recorded value(mA)   | 0.279  | 1.07   | 1.93   | 1.58   |  |  |  |  |  |
| as % of rated AC   | 0.005% | 0.016% | 0.031% | 0.024% |  |  |  |  |  |
| Limit  | 0.25%  | 0.25%  | 0.25%  | 0.25%  |  |  |  |  |  |

**Power Quality** – **Power factor:** This test shall be carried out in accordance with EN

50538 Annex D.3.4.1 but with nominal voltage -6% and +10%. Voltage to be

maintained within  $\pm 1.5\%$  of the stated level during the test.

|                    | 216.2V | 230V   | 253V   |  |
|--------------------|--------|--------|--------|--|
| 20% of Registered  | 0.9911 | 0.9899 | 0.9853 |  |
| 50% of Registered  | 0.9967 | 0.9962 | 0.9959 |  |
| 75% of Registered  | 0.9977 | 0.9971 | 0.9967 |  |
| 100% of Registered | 0.9982 | 0.9979 | 0.9973 |  |
| Limit (Leading)    | >0.95  | >0.95  | >0.95  |  |
| Limit (Lagging)    | >0.98  | >0.98  | >0.98  |  |

**Protection. Frequency tests** These tests should be carried out in accordance with EN 50438 Annex D.2.4 and the notes in EREC G98/NI Annex A1 A 1.3.2 (Inverter connected) or Annex A2 A.2.2.2 (Synchronous)

| Function | Setting   |               | Trip test |               | "No trip tests" |                    |  |
|----------|-----------|---------------|-----------|---------------|-----------------|--------------------|--|
|          | Frequency | Time<br>delay | Frequency | Time<br>delay | Frequency /time | Confirm<br>no trip |  |



| U/F | 48Hz | 0.5s | 48Hz | 0.52s | 48.2Hz/ 25s   | Confirm |
|-----|------|------|------|-------|---------------|---------|
|     |      |      |      |       | 47.8Hz/ 0.45s | Confirm |
| O/F | 52Hz | 1.0s | 52Hz | 1.03s | 51.8Hz/120s   | Confirm |
|     |      |      |      |       | 52.2Hz/ 0.98s | Confirm |

**Protection. Voltage tests** These tests should be carried out in accordance with EN 50438 Annex D.2.3 and the notes in EREC G98/NI Annex A1 A 1.3.1 (Inverter connected) or Annex A2 A.2.2.1 (Synchronous)

| Function    | Setting |               | Trip test |               | "No trip tests" |                    |  |
|-------------|---------|---------------|-----------|---------------|-----------------|--------------------|--|
|             | Voltage | Time<br>delay | Voltage   | Time<br>delay | Voltage /time   | Confirm<br>no trip |  |
| U/V stage 1 | 195.5V  | 3s            | 195.2V    | 3.03s         | 199.5V/5s       | Confirm            |  |
| U/V stage 2 | 138V    | 2s            | 138.3V    | 2.06s         | 142V/2.5s       | Confirm            |  |
|             |         |               |           |               | 134V/1.95s      | Confirm            |  |
| O/V stage 1 | 253V    | 0.5s          | 252.7V    | 0.56s         | 249V/5.0s       | Confirm            |  |
|             |         |               |           |               | 257V/0.45s      | Confirm            |  |

Note for Voltage tests the Voltage required to trip is the setting  $\pm 3.45$  V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting  $\pm 4$  V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

**Protection. Loss of Mains test.** For PV Inverters shall be tested in accordance with BS EN62116. Other Inverters should be tested in accordance with EN 50438 Annex D.2.5

at 10%,55% and 100% of rated power.

Note: Inverter tested according to BS EN 62116.

| Test Power and           | 33%    | 66%     | 100%    | 33%    | 66%     | 100%    |
|--------------------------|--------|---------|---------|--------|---------|---------|
| imbalance                | -5% Q  | -5% Q   | -5% P   | +5% Q  | +5% Q   | +5% P   |
| Trip time. Limit is 0.5s | 84.6ms | 178.3ms | 232.4ms | 86.5ms | 174.6ms | 233.5ms |

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**Protection. Frequency change, Stability test** This test should be carried out in accordance with EREC G98/NI Annex A1 A 1.3.5 (Inverter connected) or Annex A2 A.2.2.5 (Synchronous).

|                       | Start<br>Frequency | Change      | End<br>Frequency | Confirm no trip |
|-----------------------|--------------------|-------------|------------------|-----------------|
| Positive Vector Shift | 49.5Hz             | +50 degrees |                  | Confirm         |
| Negative Vector Shift | 50.5Hz             | - 50degrees |                  | Confirm         |

**Protection** – **Frequency change, RoCoF Stability test:** The requirement is specified in section 11.3, test procedure in Annex A 1.3.5 (Inverter connected) or Annex A2 A.2.2.5 (Synchronous).

|                             | Ramp range   | Test<br>frequency | Test Duration | Confirm no<br>trip |
|-----------------------------|--------------|-------------------|---------------|--------------------|
| Positive Frequency drift    | 49Hz to 51Hz | +0.95Hz/sec       | 2.1s          | Confirm            |
| Negative Frequency<br>drift | 51Hz to 49Hz | -0.95Hz/sec       | 2.1s          | Confirm            |

| Protection – Limited Frequency Sensitive Mode – Overfrequency test: This test |                     |                      |                      |                |  |  |
|---|---------------------|----------------------|----------------------|----------------|--|--|
| should be carrie  | d out in accordan   | ce with EN 50438     | Annex D.3.3 Powe     | r response to  |  |  |
| over- frequency   | . The test should b | be carried out using | g the specific thres | hold frequency |  |  |
| of 50.2 Hz and o  | dro op of 10%.      |                      |                      |                |  |  |
| Test sequence   | Measured            | Frequency            | Primary Power        | Active Power   |  |  |
| at Registered   | Active Power        |                      | Source               | Gradient       |  |  |
| Capacity >80%   | Output              |                      |                      |                |  |  |
| Step a) 50.00   | 1499.25 W           | 50Hz                 |                      | -              |  |  |
| Hz ±0.01Hz  | 1400.20 W           | 50112                |                      |                |  |  |
| Step b) 50.25   | 1484.50 W           | 50.25Hz              |                      | -              |  |  |
| Hz ±0.05Hz  | 1404.00 W           | 50.25112             |                      |                |  |  |
| Step c) 50.70   | 1352.00W            | 50.7Hz               |                      | -              |  |  |
| Hz ±0.10Hz  | 1002.0011           | 30.7112              |                      |                |  |  |
| Step d) 51.15   | 1213.64W            | 51.15Hz              |                      | -              |  |  |
| Hz ±0.05 Hz   | 1210.0411           | 01.10112             |                      |                |  |  |
| Step e) 50.70   | 1351.26W            | 50.7Hz               |                      | -              |  |  |
| Hz ±0.10Hz  | 1001.2000           | 30.7112              |                      |                |  |  |
| Step f) 50.25   | 1485.71W            | 50.25Hz              |                      | -              |  |  |
| Hz ±0.05Hz  | 1400.7 100          | 00.20112             | _                    |                |  |  |
| Step g) 50.00   | 1498.25W            | 50Hz                 |                      | -              |  |  |
| Hz ±0.10Hz  |                     | 00112                |                      |                |  |  |
| Test sequence   | Measured            | Frequency            | Primary Power        | Active Power   |  |  |
| at Registered   | Active Power        |                      | Source               | Gradient       |  |  |
| Capacity  | Output              |                      |                      |                |  |  |
| 40% - 60%   |                     |                      |                      |                |  |  |
| Step a) 50.00   | 766.50W             | 50Hz                 |                      | -              |  |  |
| Hz ±0.01Hz  | ,                   | 00112                | 4                    |                |  |  |
| Step b) 50.25   | 759.25W             | 50.25Hz              |                      | -              |  |  |
| Hz ±0.05Hz  | , 00.2000           | 00.20112             |                      |                |  |  |



| Step c) 50.70<br>Hz ±0.10Hz  | 690.16W | 50.7Hz  |  |  |  |
|------------------------------|---------|---------|--|--|--|
| Step d) 51.15<br>Hz ±0.05 Hz | 622.48W | 51.15Hz |  |  |  |
| Step e) 50.70<br>Hz ±0.10Hz  | 690.88W | 50.7Hz  |  |  |  |
| Step f) 50.25<br>Hz ±0.05Hz  | 759.21W | 50.25Hz |  |  |  |
| Step g) 50.00<br>Hz ±0.10Hz  | 764.48W | 50Hz    |  |  |  |
| Steps as defined in EN 50438 |         |         |  |  |  |

| <b>Protection</b> – <b>Power output with falling frequency test:</b> This test should be carried out in accordance with EN 50438 Annex D.3.2 active power feed -in at under-frequency. |              |         |           |  |  |  |
|--|--------------|---------|-----------|--|--|--|
| Test sequence Measured Active Frequency Primary pow  |              |         |           |  |  |  |
|  | Power Output |         | source    |  |  |  |
| Test a) 50 Hz ±  | 1498.7W      | 50 Hz   | DC supply |  |  |  |
| 0.01 Hz  |              |         |           |  |  |  |
| Test b) Point  |              |         |           |  |  |  |
| between 49.5   | 1497.9W      | 49.55Hz | DC supply |  |  |  |
| Hz and 49.6 Hz   |              |         |           |  |  |  |
| Test c) Point  |              |         |           |  |  |  |
| between 47.5   | 1497.8W      | 47.55Hz | DC supply |  |  |  |
| Hz and 47.6 Hz   |              |         |           |  |  |  |
| NOTE: The operating point in Test (b) and (c) shall be maintained for at least 5 minutes   |              |         |           |  |  |  |

### Protection. Re-connection timer.

Test should prove that the reconnection sequence starts after a minimum delay of 20 s for restoration of voltage and frequency to within the stage 1 settings of Table 2.

| Time delay setting                              | Measured<br>delay | No reconnection when voltage or frequency is brought to just outside stage 1 limits of table 1. |           |           |           |
|---|-------------------|---|-----------|-----------|-----------|
| 60s   | 90s               | At 257V   | At 191.5V | At 47.9Hz | At 52.1Hz |
| Confirmation that the SSEG does not re-connect. |                   | Confirm   | Confirm   | Confirm   | Confirm   |

**Fault level contribution.** The requirement is specified in section 5.7, test procedure in Annex A or B 1.4.6

| For a directly coupled SSEG   |        |       | Fo                  | or a Inverter SS | EG     |
|-------------------------------|--------|-------|---------------------|------------------|--------|
| Parameter                     | Symbol | Value | Time after<br>fault | Volts            | Amps   |
| Peak Short<br>Circuit current | ip     | N/A   | 20ms                | 15.4V            | 0.421A |



| Initial Value of aperiodic  | А   | N/A | 100ms        | 7.57V   | 0.176A       |
|-----------------------------|-----|-----|--------------|---------|--------------|
| Initial symmetrical         | lk  | N/A | 250ms        | 6.28V   | 0.126A       |
| Decaying<br>(aperiodic)     | iDC | N/A | 500ms        | 5.93V   | 0.117A       |
| Reactance/Re sistance Ratio | X/R | N/A | Time to trip | 0.0039s | (in seconds) |

| <b>Self-Monitoring solid state switching</b> :No specified test requirements.<br>Refer to EREC G98/NI Annex A1 A 1.4.6 (Inverter connected).  | Yes/or NA |
|---|-----------|
| It has been verified that in the event of the solid state switching device failing to disconnect the Micro-generator, the voltage on the output side of the switching device is reduced to a value below 50 V within 0.5 s. | N/A       |

| Logic interface (input port)  | Yes/or NA |  |
|---|-----------|--|
| Confirm that an input port is provided and can be used to shut down the | Yes       |  |
| module.   | res       |  |

| Additional comments |  |  |
|---------------------|--|--|
|                     |  |  |
|                     |  |  |